

Total Pages Sent 5 JUL 16 2004

To: Technology Unit: 2816
Facsimile Number: 703-872-9306

From: W. Daniel Swayze, Jr.
Texas Instruments Incorporated
Facsimile: 972-917-4418
Phone: 972-917-5633

OFFICIAL

FACSIMILE COVER SHEET

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-872-9306 on the date shown below:


Tommie Chambers July 16, 2004
Date

| | |
|--|---|
| <input checked="" type="checkbox"/> FACSIMILE COVER SHEET <input type="checkbox"/> NEW APPLICATION <input type="checkbox"/> DECLARATION (# Pages) <input type="checkbox"/> ASSIGNMENT (# Pages) <input type="checkbox"/> FORMAL DRAWINGS <input type="checkbox"/> INFORMAL DRAWINGS <input type="checkbox"/> CONTINUATION APP'N (# Pages) <input type="checkbox"/> DIVISIONAL APP'N | <input type="checkbox"/> AMENDMENT (# Pages) <input type="checkbox"/> EOT (# Month) <input type="checkbox"/> NOTICE OF APPEAL <input type="checkbox"/> APPEAL (# Pages) <input type="checkbox"/> ISSUE FEE (# Pages) <input type="checkbox"/> PETITION TO REVIVE <input checked="" type="checkbox"/> ELECTION |
| NAME OF INVENTOR(S): Barnett | RECEIPT DATE & SERIAL NO.: Serial No.: 10/696,827 Filing Date: 10/30/2003 |
| TITLE OF INVENTION: CIRCUIT AND METHOD TO COMPENSATE FOR RMR VARIATIONS AND FOR SHUNT RESISTANCE ACROSS RMR IN AN OPEN LOOP CURRENT BIAS ARCHITECTURE | |
| TI FILE NO.: TI-36061 | DEPOSIT ACCT. NO.: 20-0668 |
| FAXED: 07/16/2004 DUE: 07/29/2004 ATTY/SECY: WDS/mc | |

This facsimile is intended only for the use of the address named and contains legally privileged and/or confidential information. If you are not the intended recipient of this telecopy, you are hereby notified that any dissemination, distribution, copying or use of this communication is strictly prohibited. Applicable privileges are not waived by virtue of the document having been transmitted by Facsimile. Any misdirected facsimiles should be returned to the sender by mail at the address indicated on this cover sheet.

Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, TX 75265

JUL 16 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

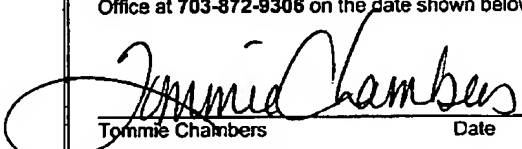
Applicant: Barnett
Serial No: 10/696,827
Filed: 10/30/2003
For: CIRCUIT AND METHOD TO COMPENSATE FOR RMR VARIATIONS
AND FOR SHUNT RESISTANCE ACROSS RMR IN AN OPEN LOOP
CURRENT BIAS ARCHITECTURE

Docket No: TI-36061
Examiner: Wells, Kenneth B.
Art Unit: 2816

OFFICIAL

RESPONSE

Assistant Commissioner For Patents
Washington, DC 20231

| | |
|--|-----------------|
| <u>CERTIFICATION OF FACSIMILE TRANSMISSION</u> | |
| I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-872-9306 on the date shown below: | |
|  Tommie Chambers | 7-16-04 Date |

Dear Sir:

Responsive to the Office Action mailed June 29, 2004 in connection with the
above identified application, Applicant(s) respectfully submit(s) the following remarks.